Panasonic

AFP0RDA4 | FP0R Intelligent units



%Photo may vary from actual product.

Spec Detail

Specifications	As of August 23, 2019 and design of the products are subject to change without notice for the product improvement.
Item	Specifications
Product Number	AFP0RDA4
Part Number	AFP0RDA4
[[OUTPUT SPECIFICATIONS]] Number of input / output channels	0 / 4
[[OUTPUT SPECIFICATIONS]]Output range (analog output setting range) : Voltage	-10 to +10 V 14 bits (-8,000 to +8,000) -5 to +5 V 14 bits (-8,000 to +8,000) 0 to +10 V 14 bits (0 to +16,000) 0 to +5 V 14 bits (0 to +16,000)
[[OUTPUT SPECIFICATIONS]]Output range (analog output setting range) : Current	0 to 20mA 14 bits (0 to +16,000) 4 to 20mA 14 bits (0 to +16,000)
[[OUTPUT SPECIFICATIONS]]Output impedance : Voltage	$0.5~\Omega$ or less
[[OUTPUT SPECIFICATIONS]]Max. output current : Voltage	±10 mA
[[OUTPUT SPECIFICATIONS]] Permissible output load resistance : Current	500 Ω or less
[[OUTPUT SPECIFICATIONS]]Max. resolution	14 bits (1/16,000)
[[OUTPUT SPECIFICATIONS]] Overall accuracy : Voltage	±0.2 % F.S. or less (at +25 °C +77 °F) ±0.4 % F.S. or less (at 0 to +55 °C +32 to +131 °F)
[[OUTPUT SPECIFICATIONS]] Overall accuracy : Current	±0.3 % F.S. or less (at +25 °C +77 °F) ±0.6 % F.S. or less (at 0 to +55 °C +32 to +131 °F)
[[OUTPUT SPECIFICATIONS]] Conversion speed	500 μs/all channels
[[OUTPUT SPECIFICATIONS]]Other functions	Compatibility function for existing programs (12 bits)
[[OUTPUT SPECIFICATIONS]] Insulation method : Between the output terminals and internal circuit	Photocoupler and isolated DC/DC converter
[[OUTPUT SPECIFICATIONS]] Insulation method : Between channels	Not insulated

© Panasonic Corporation